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PERFORMANCE OF DVR UNDER DIFFERENT VOLTAGE SAG AND SWELL CONDITIONS

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ABSTRACT

The problem of voltage sags and swells and its severe impact on sensitive loads is well known. To solve this problem, custom power devices are used. One of those devices is the Dynamic Voltage Restorer (DVR), which is one of the most efficient and effective modern custom power devices used in power distribution networks. This paper described DVR principles and voltage correction methods for balanced and/or unbalanced voltage sags and swells in a distribution system. Simulation results were presented to illustrate and understand the performances of DVR under voltage sags/swells conditions. The results obtained by simulation using MATLAB confirmed the effectiveness of this device in compensating voltage sags and swells with very fast response (relative to voltage sag/swell time).

Keywords: dynamic voltage restorer (DVR), voltage sags, voltage swells, custom power, power quality.

I. INTRODUCTION

Faults at either the transmission or distribution level may cause voltage sag or swell in the entire system or a large part of it. Also, under heavy load conditions, a significant voltage drop may occur in the system. Voltage sags can occur at any instant of time, with amplitudes ranging from 10 - 90% and a duration lasting for half a cycle to one minute [1]. Further, they could be either balanced or unbalanced, depending on the type of fault and they could have unpredictable magnitudes, depending on factors such as distance from the fault and the transformer connections. Voltage swell, on the other hand, is defined as a sudden increasing of supply voltage up 110% to 180% in RMS voltage at the network fundamental frequency with duration from 10 ms to 1 minute [2]. Voltage swells are not as important as voltage sags because they are less common in distribution systems. Voltage sag and swell can cause sensitive equipment (such as found in semiconductor or chemical plants) to fail, or shutdown, as well as create a large current unbalance that could blow fuses or trip breakers. These effects can be very expensive for the customer, ranging from minor quality variations to production downtime and equipment damage [3].

There are many different methods to mitigate voltage sags and swells, but the use of a custom Power device is considered to be the most efficient method. The concept of custom Power was introduced by N.G. Hingorani in 1995. Like Flexible AC Transmission Systems (FACTS) for transmission systems, the term custom power pertains to the use of power electronics controllers in a distribution system, especially, to deal with various power quality problems. Just as FACTS improves the power transfer capabilities and stability margins, custom power makes sure customers get pre-specified quality and reliability of supply. This pre-specified quality may contain a combination of specifications of the following: low phase unbalance, no power interruptions, low flicker at the load voltage, low harmonic distortion in

load voltage, magnitude and duration of over voltages and under voltages within specified limits, acceptance of fluctuations, and poor factor loads without significant effect on the terminal voltage.

Each of Custom Power devices has its own benefits and limitations. Dynamic Voltage Restorer (DVR) is one of the most effective type of these devices. There are numerous reasons why the DVR is preferred over the others. A few of these reasons are presented as follows. The SVC pre-dates the DVR, but the DVR is still preferred because the SVC has no ability to control active power flow [5]. Another reason is that the DVR costs less compared to the UPS. Other reasons include that the DVR has a higher energy capacity and lower costs compared to the SMES device. Furthermore, the DVR is smaller in size and costs less compared to the DSTATCOM. Based on these reasons, it is no surprise that the DVR is widely considered as an effective custom power device in mitigating voltage sags. In addition to voltage sags and swells compensation, DVR can also added other features such as harmonics and Power Factor correction. Compared to the other devices, the DVR is clearly considered to be one of the best economic solutions for its size and capabilities.

This paper introduced Dynamic Voltage Restorer (DVR) and its operating principle. Then, analyses of the voltage correction methods were presented. At the end, simulation results using MATLAB were illustrated and discussed.

2. Dynamic voltage restorer (DVR)

A Dynamic Voltage Restorer (DVR) is a series connected solid state device that injects voltage into the system in order to regulate the load side voltage. The DVR was first installed in 1996. It is normally installed in a distribution system between the supply and the critical load feeder. Its primary function is to rapidly boost up the load-side voltage in the event of a disturbance in order to avoid any power disruption to that load. There are various

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circuit topologies and control schemes that can be used to implement a DVR. In addition to voltage sags and swells compensation, DVR can also added other features such as: line voltage harmonics compensation, reduction of transients in voltage and fault current limitations.

The general configuration of the DVR consists of an Injection / Booster transformer, a Harmonic filter, a Voltage Source Converter (VSC), and a Control and Protection system as shown in Figure-1.

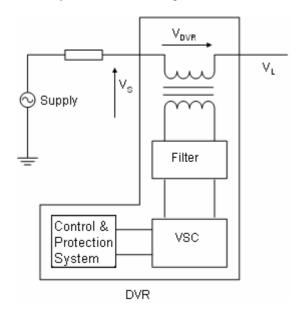


Figure-1. DVR series connected topology.

2.1 Injection / booster transformer

The Injection / Booster transformer is a specially designed transformer that attempts to limit the coupling of noise and transient energy from the primary side to the secondary side. Its main tasks are: connects the DVR to the distribution network via the HV-windings and transforms and couples the injected compensating voltages generated by the voltage source converters to the incoming supply voltage. In addition, the Injection / Booster transformer serves the purpose of isolating the load from the system (VSC and control mechanism). It is one unit three phase construction.

2.2 Harmonic filter

The main task of harmonic filter is to keep the harmonic voltage content generated by the voltage source converters to the permissible level (i.e., Eliminate high frequency switching harmonics).It has a small rating approximately 2% of the load MVA [12].

2.3 Voltage source converter

A VSC is a power electronic system consists of a storage device and switching devices, which can generate a sinusoidal voltage at any required frequency, magnitude, and phase angle. In the DVR application, the VSC is used to temporarily replace the supply voltage or to generate the part of the supply voltage which is missing.

Numerous circuit topologies are available for the VSC. A widely used method is the two level or multilevel three-phase converter which shares a dc capacitor between all phases. The purpose of this capacitor is mainly to absorb harmonic ripple and hence it has a relatively small energy storage requirement, particularly when operating in balanced conditions. The size of this capacitor has to be increased if needed to provide voltage support in unbalanced conditions. Also, as the capacitor is shared between the three phases, a sag on only one phase may cause a distortion in the injected current waveforms on the other phases.

2.3.1 Switching devices

There are four main types of switching devices: Metal Oxide Semiconductor Field Effect Transistors (MOSFET), Gate Turn-Off thyristors (GTO), Insulated Gate Bipolar Transistors (IGBT), and Integrated Gate Commutated Thyristors (IGCT). Each type has its own benefits and drawbacks. The MOSFET requires a high onresistance and has fast switching times. It is capable of working beyond the 20 kHz frequency. The limitations are that the increasing on-resistance with increasing voltage limits the device to applications with just a few hundred volts. The GTO is a latching device that can be turned off by a negative pulse of current to its gate. The GTO is best suited for high voltage applications. The disadvantages of the GTO are that GTO based devices are not able to meet the dynamic requirements of a DVR. The IGBT is considered to be a newer device compared to the MOSFET and GTO. It was first introduced in the early 1980s and has become a popular device because of its superior characteristics. In essence, it is a three terminal controllable switch that combines the fast switching times of the MOSFET with the high voltage capabilities of the GTO. The result of this combination is a medium speed controllable switch capable of supporting the medium power range.

The IGBT is a recent compact device with enhanced performance and reliability that allows building VSC with very large power ratings. Because of the highly sophisticated converter design with IGCTs, the DVR can compensate dips which are beyond the capability of the past DVRs using conventional devices.

2.3.2 Storage devices

The purpose is to supply the necessary energy to the VSC via a dc link for the generation of injected voltages. Batteries and Ultra capacitors are the most common types of energy storage devices. In fact, the capacity of the stored energy directly determines the duration of the sag which can be mitigating by the DVR. Batteries are the common choice and can be highly effective if a high voltage battery configuration is used. This high voltage string of batteries can be placed across the regulated dc bus with little or no additional circuitry. However, batteries in general have a short lifetime and often require some type of battery management system, which can be quite costly. An interesting alternative to

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batteries is the use of ultracapacitors, which have a wider voltage range than batteries and can be directly paralleled across the input bus. Ultracapacitors have a specific energy density less than that of a battery, but a specific power greater than a battery, making them ideal for short (up to several seconds) pulses of power. Certain ultracapacitors (unsyrnmetrical electrochemical) can hold charge over extended periods of time, so as to act like a battery. However, unlike batteries, these ultracapacitors have a short charge time and much longer lifetime.

2.4 Control and protection system

The control mechanism of the general configuration typically consists of Digital Signal Processing (DSP) boards. The software on the DSP board provides the controls such as detection and correction. Filters are commonly used for these purposes. The most common types of filter algorithm are the Fourier Transform (FT) and the Wavelet Transform (WT). Although, the Fourier Transform still remains the most common type.

To maximize dynamic performance, direct feedforward-type control architecture should be applied in the control concept of the DVR. With this concept a fast response time (approximately 1ms) can be achieved to compensate voltage sags.

All protective functions of the DVR should be implemented in the software. Differential current protection of the transformer, or short circuit current on the customer load side are only two examples of many protection functions possibility. Depending on the particular fault condition, the fast control and protection may switch the DVR into bypass if it becomes inoperable, thus securing an uninterrupted energy flow to the customer's plant.

3. Operating principle of DVR

The basic function of the DVR is to inject a dynamically controlled voltage V_{DVR} generated by a forced commutated converter in series to the bus voltage by means of a booster transformer. The momentary amplitudes of the three injected phase voltages are controlled such as to eliminate any detrimental effects of a bus fault to the load voltage V_L . This means that any differential voltages caused by disturbances in the ac feeder will be compensated by an equivalent voltage generated by the converter and injected on the medium voltage level through the booster transformer.

The DVR works independently of the type of fault or any event that happens in the system, provided that the whole system remains connected to the supply grid, i.e. the line breaker does not trip. For most practical cases, a more economical design can be achieved by only compensating the positive and negative sequence components of the voltage disturbance seen at the input of the DVR (because for a typical distribution bus configuration, the zero sequence part of a disturbance will not pass through the step down transformer which has infinite impedance for this component).

The DVR has two modes of operation which are: standby mode and boost mode. In standby mode (V_{DVR} =0), the booster transformer's low voltage winding is shorted through the converter. No switching of semiconductors occurs in this mode of operation, because the individual converter legs are triggered such as to establish a short-circuit path for the transformer connection. Therefore, only the comparatively low conduction losses of the semiconductors in this current loop contribute to the losses. The DVR will be most of the time in this mode. In boost mode (V_{DVR} >0), the DVR is injecting a compensation voltage through the booster transformer due to a detection of a supply voltage disturbance.

4. DVR voltage correction methods

The possibility of compensating voltage sag can be limited by a number of factors including finite DVR power rating, different load conditions and different types of voltage sag. Some loads are very sensitive to phase angle jump and others are tolerant to it. Therefore, the control strategy depends on the type of load characteristics. There are three different methods for DVR voltage correction which are presented below.

4.1 Pre-dip compensation (PDC)

The PDC method tracks the supply voltage continuously and compensates load voltage during fault to pre-fault condition. In this method, the load voltage can be restored ideally, but the injected active power cannot be controlled and it is determined by external conditions such as the type of faults and load conditions. This method is achieved by using a fault detector to freeze the output from the Phase Locked Loop (PLL) circuit, when the fault occurs. Then, the frozen angle is used to restore the previous balanced load voltages by using the Park transform [6, 24, 25]. The lack of the negative sequence detection in this method leads to the phase-oscillation in the case of single-line faults. Figure-2 shows the single-phase vector diagram of this method.

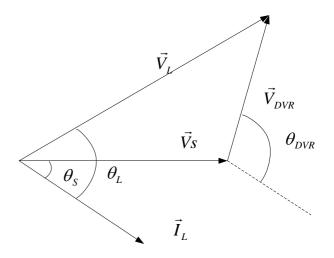


Figure-2. Single-phase vector diagram of the PDC method.

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According to Figure-2, the apparent power of DVR is

$$S_{DVR} = 3I_L V_{DVR}$$
$$= I_L \sqrt{V_L^2 + V_S^2 - 2V_L V_S \cos(\theta_L - \theta_S)}$$

And the active power of DVR is

$$P_{DVR} = 3I_L (V_L \cos \theta_L - V_S \cos \theta_S)$$

The magnitude and the angle of DVR voltage are

$$V_{DVR} = \sqrt{V_L^2 + V_S^2 - 2V_L V_S \cos(\theta_L - \theta_S)}$$

$$\theta_{DVR} = \tan^{-1} \left(\frac{V_L \sin \theta_L - V_S \sin \theta_S}{V_L \cos \theta_L - V_S \cos \theta_S} \right)$$

4.2 In-Phase compensation (IPC)

This is the most straight forward and used method in which the injected DVR voltage is in phase with the supply side voltage regardless of the load current and the pre-fault voltage as shown in Figure-3. The IPC method is suitable for minimum voltage or minimum energy operation strategies [24]. In other word, this approach requires large amounts of real power to mitigate the voltage sag, which means a large energy storage device.

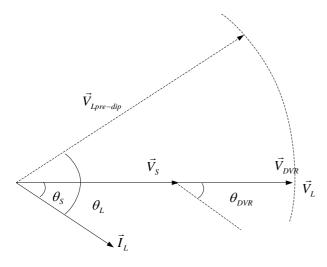


Figure-3. Phasors diagram of the IPC method.

According to Figure-3, the apparent power of DVR is

$$S_{DVR} = 3I_L V_{DVR} = 3I_L (V_L - V_S)$$

And the active power of DVR is

$$P_{DVR} = 3I_L V_{DVR} \cos \theta_S = 3I_L (V_L - V_S) \cos \theta_S$$

The magnitude and the angle of the DVR voltage are

$$V_{DVR} = V_L - V_S$$
$$\theta_{DVR} = \theta_S$$

4.3 In phase advance compensation (IPAC)

Pre-Dip and in-phase compensation method must inject active power to loads to correct voltage disturbance. However, the amount of possible injection active power is confined to the stored energy in DC link, which is one of the most expensive components in DVR. Due to the limit of energy storage capacity of DC link, the DVR restoration time and performance are confined in these methods.

For the sake of controlling injection energy, in phase advance compensation method was proposed. The injection active power is made zero by means of having the injection voltage phasor perpendicular to the load current phasor. This method can reduce the consumption of energy stored in DC link by injecting reactive power instead of active power. Reducing energy consumption means that ride-through ability is increased when the energy storage capacity is fixed. On the other hand, the injection voltage magnitude of in-phase advance compensation method is larger than those of pre-dip or inphase compensation methods and the voltage phase shift can cause voltage waveform discontinuity, inaccurate zero crossing and load power swing. Therefore, in phase advance compensation method should be adjusted to the load that is tolerant to phase angle jump, or transition period should be taken while phase angle is moved from pre-fault angle to advance angle.

In short, IPAC method uses only reactive power and unfortunately, not all the sags can be mitigated without real power, as a consequence, this method is only suitable for a limited range of sags.

5. Simulation

In order to show the performance of the DVR in voltage sags and swells mitigation, a simple distribution network is simulated using MATLAB (Figure-1). Voltage sags and swells are simulated by temporary connection of different impedances at the supply side bus. A DVR is connected to the system through a series transformer with a capability to insert a maximum voltage of 50% of the phase to ground system voltage. Apart from this, a series filter is also used to remove any high frequency components of power. In this simulation the In-Phase Compensation (IPC) method is used. The load considered in the study is a 5.5 MVA capacity with 0.92 p.f., lagging.

5.1 Voltage sags

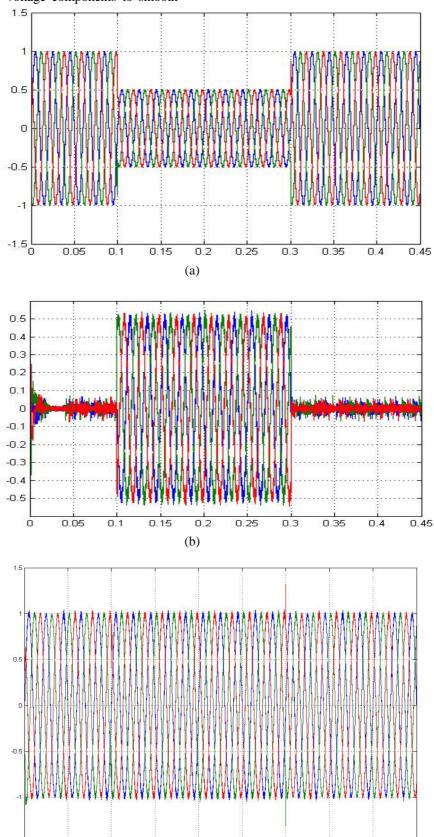
A case of Three-phase voltage sag is simulated and the results are shown in Figure-4. Figure-4(a) shows a 50% voltage sag initiated at 100 ms and it is kept until 300 ms, with total voltage sag duration of 200 ms. Figure-4(b) and (c) show the voltage injected by the DVR and the compensated load voltage, respectively. As a result of DVR, the load voltage is kept at 1 p.u. throughout the simulation, including the voltage sag period. Observe that

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during normal operation, the DVR is doing nothing. It quickly injects necessary voltage components to smooth



 $\begin{tabular}{ll} (c) & t \ (s) \\ \hline \textbf{Figure-4.} \ Three-phase \ voltages \ sag: (a)-source \ voltage, (b)-injected \ voltage, (c)-load \ voltage. \\ \hline \end{tabular}$



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In order to understand the performance of the DVR under unbalanced conditions, Single-phase voltage sag at supply bus bar is simulated and the results are shown in Figure-5. The supply voltage with one phase voltage dropped down to 50% is shown in Figure-5(a). The DVR injected voltage and the load voltage are shown

in Figure-5(b) and (c), respectively. As can be seen from the results, the DVR is able to produce the required voltage components for different phases rapidly and help to maintain a balanced and constant load voltage at 1.00 p.u.

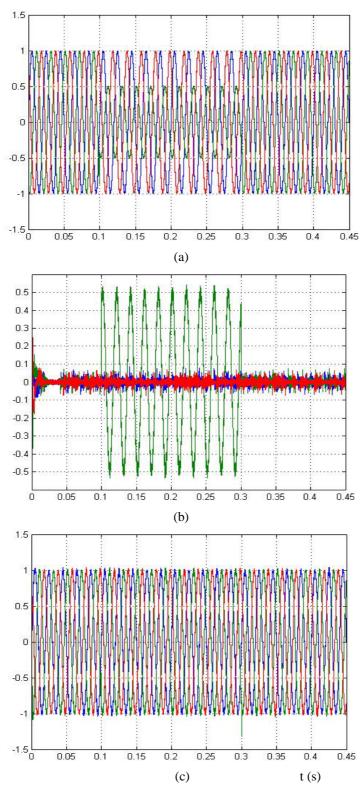


Figure-5. Single-phase voltage sag: (a)-source voltage, (b)-injected voltage, (c)-load voltage



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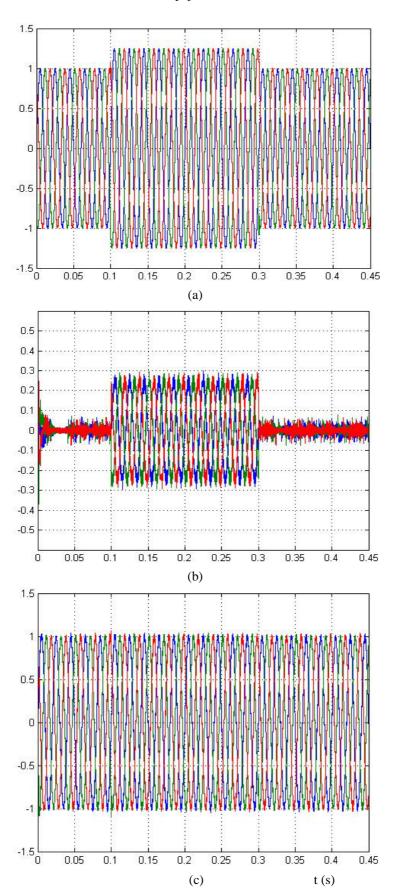


Figure-6. Three-phase voltages swell: (a)-source voltage, (b)-injected voltage, (c)-load voltage.



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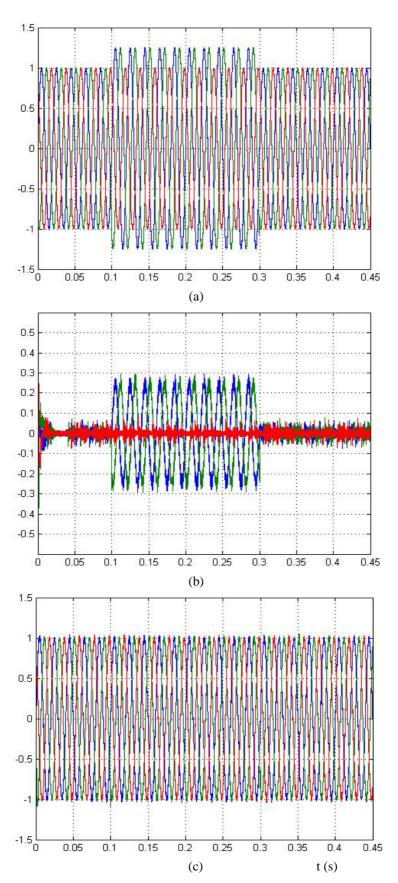


Figure-7. Two-phase voltages swell: (a)-source voltage, (b)-injected voltage, (c)-load voltage.

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5.2 Voltage swells

The performance of DVR for a voltage swell condition is investigated. Here, the supply voltage swell is generated as shown in Figure-6(a). The supply voltage amplitude is increased about 125% of nominal voltage. The injected voltage that is produced by DVR in order to correct the load voltage and the load voltage are shown in Figure-6(b) and (c), respectively. As can be seen from the results, the load voltage is kept at the nominal value with the help of the DVR. Similar to the case of voltage sag, the DVR reacts quickly to inject the appropriate voltage component (negative voltage magnitude) to correct the supply voltage.

The performance of the DVR with an unbalanced voltage swell is shown in Figure-7. In this case, two of the three phases are higher by 25% than the third phase as shown in Figure-7(a). The injected voltage that is produced by DVR in order to correct the load voltage and the load voltage are shown in Figure-7(b) and (c), respectively. Notice the constant and balanced voltage at the load throughout the simulation, including during the unbalanced voltage swell event.

6. CONCLUSIONS

In this paper an overview of DVR is presented. DVR is an effective custom power device for voltage sags and swells mitigation. The impact of voltage sags on sensitive equipment is severe. Therefore, DVR is considered to be an efficient solution due to its relatively low cost and small size, also it has a fast dynamic response.

The simulation results show clearly the performance of a DVR in mitigating voltage sags and swells. The DVR handles both balanced and unbalanced situations without any difficulties and injects the appropriate voltage component to correct rapidly any anomaly in the supply voltage to keep the load voltage balanced and constant at the nominal value.

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