



SIMULATION AND EVALUATION OF PERFORMANCE PARAMETERS FOR PWM BASED INTERLEAVED BOOST CONVERTER FOR FUEL CELL APPLICATIONS

M. Tamilarasi¹ and R. Seyezhai²

¹Department of Electrical and Electronics Engineering, Kongu Engineering College, Tamilnadu, India

²Department of Electrical and Electronics Engineering, SSN College of Engineering, Tamilnadu, India

E-Mail: tamilarasi_1979@yahoo.com

ABSTRACT

In the present scenario, renewable energy sources are playing an important role to satisfy the requirements of customer's demand. Among these sources, fuel cell is an attractive technology as it provides a clean and efficient source of electricity. But a suitable power conditioner is required to convert the DC power of fuel into useful AC power. This paper focuses on a four-phase directly coupled Interleaved Boost Converter (IBC) as a suitable interface for fuel cell applications based on pulse width modulation technique. The proposed converter is simulated in MATLAB/SIMULINK. Input current, inductor current and output voltage ripple is studied. Various energy factors have been computed and the results are verified.

Keywords: interleaved boost converter, PWM, energy factor and ripple.

1. INTRODUCTION

Among the renewable energies, fuel cell is a rapidly developing technology and it is preferred because of its high conversion efficiency. The output of a single fuel cell is about 0.6 -0.7V, therefore a series connection of fuel cells (stack) is required to get a higher output. But a bigger stack will increase the cost of the system and hence to overcome this, a suitable DC-DC converter is preferred. Different configurations of DC-DC converters have been discussed by many researchers [1, 2, 3], but this paper focuses on the four-phase IBC[5] employing PWM technique to reduce the ripples both at the input and output side. IBC has number of benefits such as harmonic cancellation, better efficiency, better thermal performance and high power density. The concept of PWM generation for IBC is discussed. Design equations and ripple calculations are carried out for the proposed converter. The performance of the converter is analyzed by computing the energy factor parameters and the theoretical results are verified through MATLAB/SIMULINK.

2. FOUR-PHASE INTERLEAVED BOOST CONVERTER

The circuit diagram for the 4-phase directly coupled IBC with PWM generator is shown in Figure-1. In the proposed IBC, coupled inductors[14,15] are used to improve the performance of the converter. Also, coupled inductors results in reduced core and winding losses, improved input and inductor current ripple[6,7]. The gating signals are generated by using PWM technique. PWM uses a rectangular pulse whose pulse width is modulated resulting in the variation of the average values of the waveform.

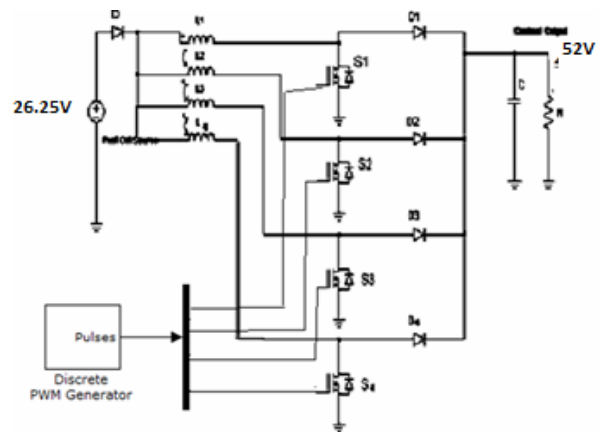


Figure-1. Circuit diagram for 4 _phase direct coupled IBC with PWM generator.

3. PWM GENERATION METHOD

Consider a pulse waveform $f(t)$ as shown in Figure-2 with a low value y_{min} , a high value of the waveform is given by,

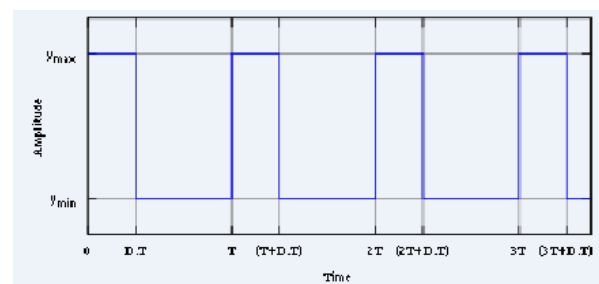


Figure-2. Average value of PWM waveform.



$$y = \frac{1}{T} \int_0^T f(t) dt \quad (1)$$

As $f(t)$ is a pulse wave, its value is

$$y_{\max} \text{ for } 0 < t < D \cdot T \quad 2(a)$$

$$y_{\min} \text{ for } D \cdot T < t < T \quad 2(b)$$

The expression for 'y' is given by

$$y = \frac{1}{T} \left[\int_0^{DT} y_{\max} dt + \int_{DT}^T y_{\min} dt \right] \\ = \frac{DT y_{\max} + T(1-D)y_{\min}}{T} \\ y = D \cdot y_{\max} + (1-D) \cdot y_{\min} \quad (3)$$

The equation (3) can be simplified in many cases where $y_{\min} = 0$ as $y = D \cdot y_{\max}$. It's evident that the average value of the signal y is directly dependent on the duty cycle D .

The PWM generator block generates pulses for carrier based pulse width modulation converters using two level topology. The block can be used to fire the forced commutated devices. The pulses are generated by comparing a triangular carrier waveform to a reference modulating signal. The modulating signal can be generated by the PWM generator itself, or they may be a vector of external signals connected at the input of the block. One reference signal is needed to generate the pulses for a single or two arm bridges three reference signals are needed to generate the pulses for a three phase single or double bridge. The amplitude, phase and frequency of the reference signals are set to control the output voltage of the bridge connected to the PWM generator block.

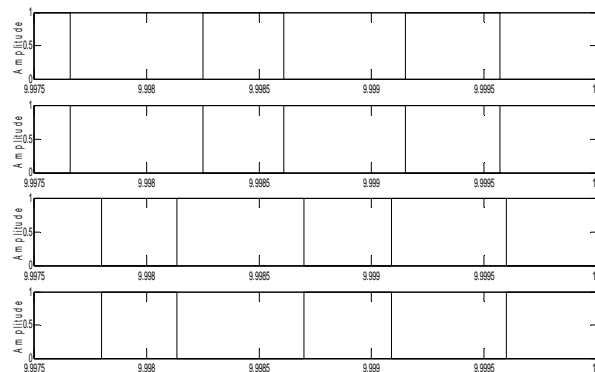


Figure-3. Gating pattern for the switches of IBC.

4. DESIGN EQUATIONS FOR THE PROPOSED IBC

The steps for designing IBC are as follows[8]:

- Design of inductor[16]
- Design of capacitor[16]
- Choice of devices[16]
- Selection of duty ratio and number of phases [16]

(i) Design of coupled inductors [12,13]

The phase shift of 90° can be achieved between each phase leg. The equivalent inductance expression for the direct coupled IBC is [1].

$$L_{eq} = \frac{V_{in} DT}{\Delta I_{phase}} \quad (4)$$

Where

V_{in} = Input voltage

D = Duty ratio

ΔI_{phase} = Phase ripple current

$$\Delta I_{phase} = \frac{V_{in} DT}{L} \cdot \frac{1 + \alpha + 2\alpha \frac{D}{1-D}}{1 + \alpha - 2\alpha^2} \quad (5)$$

The self inductance of the coupled inductor is found as [1],

$$L = \frac{1 + \alpha \frac{D}{1-D}}{1 + \alpha - 2\alpha^2} L_{eq} \quad (6)$$

The value of L_m and L_k is calculated as [1],

$$L_m = \alpha \cdot L \quad (7)$$

$$L_k = (1 - \alpha) \cdot L \quad (8)$$

Overall input current ripple is [1]

$$\Delta I_{in} = \frac{V_{in} DT}{L} \cdot \left[\frac{(1 - \alpha) \left(1 - \frac{2D}{1-D} \right)}{1 + \alpha - 2\alpha^2} \right] \quad (9)$$

To reduce the input current ripple and to keep the phase current ripple within limits the coupling co-efficient can be carefully chosen as 0.61.

(ii) Design of capacitor

A capacitor filter is needed at the output to limit the peak to peak ripple. The capacitance value is [13],

$$C = \frac{V_o DT}{R \Delta V_o} \quad (10)$$

V_o = Output voltage

D = Duty ratio

T = Switching period

R = Load resistance

ΔV_o = Output voltage ripple

(iii) Choice of devices

To improve the electrical performance of the converter, the proper choice of semiconductor materials is important.



(iv) Choice of phase and duty ratio

For any particular input, output voltages and power ratings of the converter is given as,

$$D = \frac{V_o - V_{in}}{V_o} \tag{11}$$

Using the above equations, the proposed IBC is designed.

5. SIMULATION RESULTS

The simulation of the four-phase direct coupled IBC[9,10] with PWM generator is carried out using MATLAB/SIMULINK. The parameters of the circuit are shown in Table-1.

Table-1. Simulation parameters for 4-phase direct coupled IBC.

| | |
|-----------------------|---------|
| Input voltage | 26.25V |
| Output voltage | 52V |
| R | 1.6Kohm |
| C | 0.049F |
| f_s | 30KHz |
| L_m | 1.1529H |
| $L_{k1} = L_{k2}$ | 0.737H |
| D | 0.52 |
| A | 0.61 |
| I_1 | 76.19A |
| I_2 | 33.33A |
| I_L | 6.25A |
| Voltage transfer gain | 2.08 |

The output voltage waveform for 4 phase direct coupled IBC with PWM generator is shown in Figure-4.

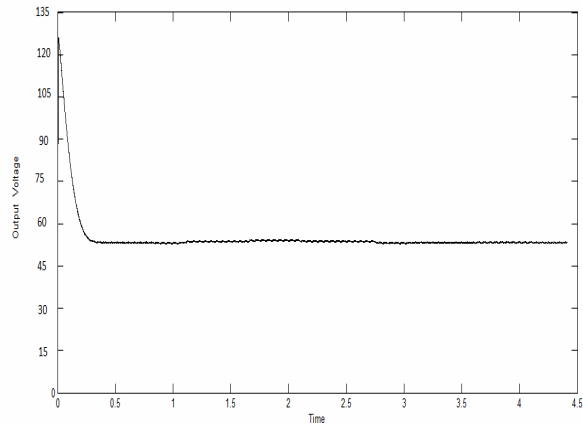


Figure-4. Output voltage for 4-phase direct coupled IBC with PWM generator.

The input current ripple waveform for 4 phase direct coupled IBC with PWM generator is shown in Figure-5.

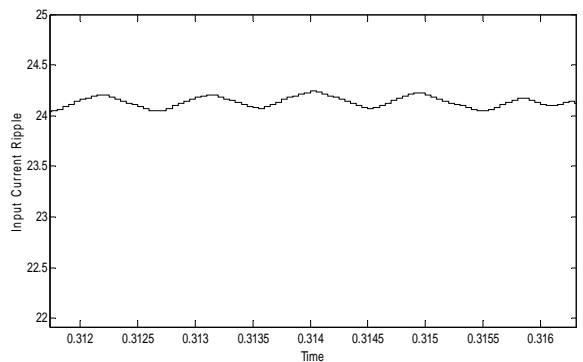


Figure-5. Input current ripple for 4 phase direct coupled IBC with PWM generator.

Figure-6 depicts that the output voltage ripple waveform for 4 phase direct coupled IBC with PWM generator.

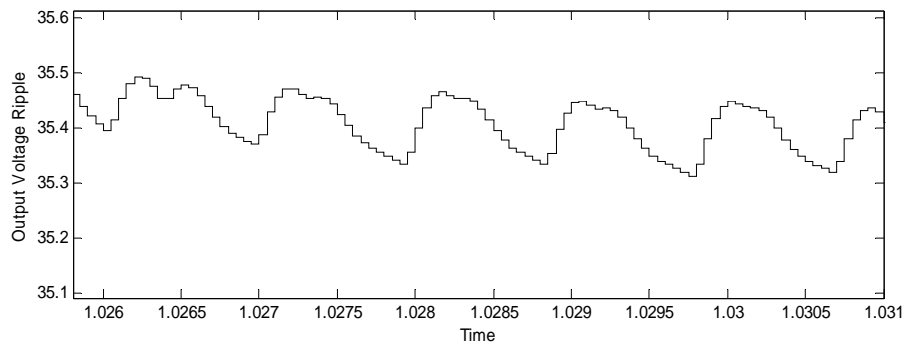


Figure-6. Output voltage ripple waveform for 4-phase direct coupled IBC with PWM generator.



Table-2 shows the ripples for PWM based IBC.

Table-2. Ripple calculation for PWM based IBC.

| Performance parameter | PWM generator |
|-----------------------|---------------|
| Input current ripple | 0.012A |
| Output voltage ripple | 0.0056V |

6. ENERGY FACTOR CALCULATIONS FOR IBC

The energy factor and sub-sequential parameters are calculated in this section. The instantaneous and average values of input voltage and current are assumed as V_1 , I_1 , $v_1(t)$, $i_1(t)$. Similarly the instantaneous output voltage, current and the average values are assumed as V_2 , I_2 , $v_2(t)$, $i_2(t)$. The various energy factor parameters are given by the following equations.

(a) Pumping energy (PE)

Pumping energy is the transfer of energy from source to energy storage elements like inductor and capacitor in a switching period T [4].

$$\begin{aligned} PE &= \int_0^T P_{in}(t) dt \\ &= \int_0^T v_1(t) i_1(t) dt \\ &= V_1 I_1 T \end{aligned} \quad (12)$$

(b) Stored energy (SE)

Stored energy is the amount of energy which is stored in the inductors and capacitor [4].

$$W_L = 0.5 L I_L^2 \quad (13)$$

$$W_C = 0.5 C V_C^2 \quad (14)$$

If the number of inductors and capacitors are assumed as n_1 and n_2 , the total stored energy is, [4].

$$SE = \sum_{j=1}^{n_1} W_{Lj} + \sum_{j=1}^{n_2} W_{Cj} \quad (15)$$

(c) Capacitor-inductor stored energy ratio (CIR)

It's the ratio between capacitor stored energy to the inductor stored energy.

$$CIR = \frac{\sum_{j=1}^{n_2} W_{Cj}}{\sum_{j=1}^{n_1} W_{Lj}} \quad (16)$$

(d) Stored energy variation on inductors and capacitor (VE)

The variation of stored energy on the inductor is given by,

$$\Delta W_L = L I_L \Delta i_L \quad (17)$$

Δi_L = Ripple in the inductor current

The variation of stored energy on the capacitor is given as,

$$\Delta W_C = L V_C \Delta v_C \quad (18)$$

Δv_C = Variation of voltage across capacitor (ripple). Hence the total variation,

$$= \sum_{j=1}^{n_1} \Delta W_{Lj} + \sum_{j=1}^{n_2} \Delta W_{Cj} \quad (19)$$

(e) Energy factor (EF)

Energy factor is the ratio between stored energy to the pumping energy. It's inversely proportional to the switching frequency,

$$EF = \frac{\sum_{j=1}^{n_1} W_{Lj} + \sum_{j=1}^{n_2} W_{Cj}}{V_1 I_1 T} \quad (20)$$

(f) Variation energy factor (EF_v)

Variation energy factor is the ratio between variations of stored energy to the variation of the pumping energy [4].

$$\begin{aligned} EF_v &= \frac{VE}{PE} \\ &= \frac{\sum_{j=1}^{n_1} \Delta W_{Lj} + \sum_{j=1}^{n_2} \Delta W_{Cj}}{V_1 I_1 T} \end{aligned} \quad (21)$$

(g) Time constant (τ)

Time constant is used to describe the transient process of the converter.

$$\tau = \frac{2T * EF}{1 + CIR \left[1 + CIR * \frac{(1-\eta)}{\eta} \right]} \quad (22)$$

If there is no power loss in converter means $\eta = 1$.

(h) Damping time constant (τ_d)

This parameter is used to estimate, the oscillation response for unit step or impulse interference. It's also used to describe the transient process of the converter.

$$\tau_d = \frac{2T * EF}{1 + CIR \left[\frac{CIR}{\eta + CIR(1-\eta)} \right]} \quad (23)$$

(i) Time constant ratio (ξ)

It's also used to analyze the transient process of the converter. [4].

$$\begin{aligned} \xi &= \frac{\tau_d}{\tau} \\ &= \frac{CIR}{\left(1 + CIR * \frac{(1-\eta)}{\eta} \right)^2} \end{aligned} \quad (24)$$

Using the above equations, the energy factor and the sub-sequential parameters were computed for the



proposed 4- phase directly coupled IBC with PWM generator and the values are tabulated in Table-3.

Table-3. Energy factor parameters for IBC.

| Parameter | Values |
|--|------------|
| Pumping energy (PE) | 0.0659 J |
| Stored energy in the inductor | 0.0410 J |
| Stored energy in the capacitor | 88.2 J |
| Total stored energy (SE) | 88.36 J |
| Capacitor-inductor stored energy ratio (CIR) | 537.8 |
| Time constant | 0.1064 sec |
| Damping time constant | 0.0016 sec |
| Time constant ratio | 0.015 |
| Variation in stored energy in the Inductor | 0.0086 J |
| Variation in stored energy in the capacitor | 0.0338 J |
| Total variation in the stored energy (VE) | 0.0682 J |
| Energy factor (EF) | 1340.8 |
| Variation energy (EFv) | 1.03 |

7. CONCLUSIONS

In this paper, a four- phase directly coupled IBC with PWM generator has been designed. Employing coupled inductor in the proposed converter resulted in reduced ripples at the input side which is found from the simulation results. Also, PWM based IBC gave a reduced voltage ripple at the output side. From the energy factor calculation, it is obvious how effectively the converter operates and these values are useful in designing a digital controller for the converter.

REFERENCES

- [1] Seyezhai R and Mathur B.L. 2011. Analysis Design and Experimentation of Interleaved Boost Converter for fuel cell power source. *International Journal of Research and Reviews in Information Science*. 1(2).
- [2] Seyezhai R and Mathur B.L. 2011. A compararision of three phase uncoupled and directly coupled Interleaved Boost converter for fuel cell applications. *International Journal on Electrical Engineering and Informatics*. 3(3): 394.
- [3] Arinee M, Nagarajan V., S, Dimple, Seyezhai R and Mathur B.L. 2011. Modeling and design of fuel cell based two phase Interleaved Boost Converter. In *Proceedings of first International conference on electrical energy systems*. pp. 72-77.
- [4] Radha Sree. K and Seyezhai. R. 2012. Design and Computation Of energy Factor Parameters for an Interleaved Boost Converter. *International Journal of Engineering Research and Applications*. 2(5): 1416-1420, September-October.
- [5] P. A. Dahono, S. Riyadi, A. Mudawari and Y. Haroen. 1999. Output ripples analysis of multiphase DC-DC converter. *IEEE Int. Conf. Power Electrical and Drive Systems, Hong Kong*. pp. 626-631.
- [6] P. Lee, Y. Lee, D. K. W. Cheng and X. Liu. 2000. Steady-state analysis of an interleaved boost converter with coupled inductors. *IEEE Trans. Industrial Electronics*. pp. 787-795.
- [7] M. Veerachary T. Senjyu and K. Uezato. 2001. Small-signal analysis of interleaved dual boost converter. *Int. Journal of circuit theory and applications*. 29(6): 575-589.
- [8] M. Veerachary, T. Senjyu and K. Uezato. 2001. Modeling and analysis of interleaved dual boost converter. *Int. Conf. IEEE International Symposium on Industrial Electronics*. 2: 718-722.
- [9] H. Xu, E. Qiao, X. Guo, X. Wen and L. Kong. 2005. Analysis and Design of High Power Interleaved Boost Converters for Fuel Cell Distributed Generation System. *Int. Conf. IEEE Power Electronics Specialists Conference (PESC)*. pp. 140-145.
- [10] H. B. Shin, J. G. Park, S. K. Chung, H. W. Lee and T. A. Lipo. 2005. Generalized Steady-State Analysis of Multiphase Interleaved Boost Converter with Coupled Inductors. *Proc. IEEE electronics Power Application*. 152(3): 584-594.
- [11] R. J. Wai and R. Y. Duan. 2005. High step-up converter with coupled-inductor. *IEEE Trans. Power Electronics*. 20(5): 1025-1035.
- [12] Gyu-yeongchoe S. K. Hyun and W. Y. Lee. 2007. Design consideration of Interleaved Converters for fuel cell applications. *Int. Conf. Electrical Machines and Systems, Seoul*. pp. 238-243.
- [13] P. Thounthong, P. Sethakul, S. Rael and B. Davat. 2008. Design and implementation of 2- phase interleaved boost converter for fuel cell power source. *Int. Conf. Power Electronics, Machines, and Drives, PEMD 2008*. pp. 91-95.
- [14] H. Kosai, S. McNeal, Austin Page, Brett Jordan, Jim Scofield and B. Ray. 2009. Characterizing the effects of inductor coupling on the performance of an interleaved boost converter. *Proc. CARTS USA 2009*. pp. 237-251.
- [15] Laszlo Huber, T. Brian Irving and M. Milan Jovanović. 2009. Closed-Loop Control Methods for Interleaved DCM/CCM Boundary Boost PFC Converters. *Int. Conf. IEEE Applied Power Electronics, Washington*. pp. 991-997.



- [16] Seyezhai R. Design consideration of Interleaved Boost Converter for Fuel cell Systems. International Journal of Advanced Engineering Science and Technologies. 7(2): 323-329.